

<b>Notice of References Cited</b>	Application/Control No. 09/834,342		Applicant(s)/Patent Under Reexamination LAHIRI ET AL.	
	Examiner Fred Ferris		Art Unit 2128	Page 1 of 1

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*	X	"Retargetable Cache Simulation Using High Level Processor Models", R. Ravindran et al, IEEE 0-7695-0954-1/01, IEEE January 2001			

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.